EFS ID:	1043029				
Application Number:	10605672				
Confirmation Number:	2671				
Title of Invention:	HIGH PERFORMANCE STRAINED CMOS DEVICES				
First Named Inventor:	Bruce B. Doris				
Correspondence Address:	Andrew M. Calderon Greenblum and Berstein P.L.C 1950 Roland Clarke Place - Reston VA 20191 US 703-716-1191				
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Payment information:

Submitted with Payment	yes
Payment was successfully received in RAM	\$790.0

RAM confirmation Number	271
Deposit Account	090458

The Director of the USPTO is hereby authorized to charge indicated fees and credit any overpayment as follows: Charge any Additional Fees required under 37 C.F.R. Section 1.16 and 1.17

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages				
1		P27161_RCE.pdf	6714933	yes	123				
	Multipart Description								
	Doc Desc		Start	End					
	Request for Continued Examination (RCE)		1	1					
	Amendment - After Non-Final Rejection		2	9					
	Information Disclosure Statement (IDS) Filed		10	13					
	Foreign Reference		14	19					
	NPL Documents		20	123					
Warnings:									
Information:									
2	Fee Worksheet (PTO-875)	fee-info.pdf	8166	no	2				
Warnings:		ı							
Information:									
		Total Files Size (in bytes):	67	23099					

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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.